MODEL 864A
INTERFACE DESCRIPTION

PIN	SIGNAL NAME	DESCRIPTION
1	+24 VOLT INPUT	24 VDC at up to 3.5 A required to power module. Filtered by 3 mH common mode EMI suppression choke (reference Figure 1).
2	+24 VOLT RETURN	Main 24 V power return. Filtered by 3 mH common mode EMI suppression choke (reference Figure 1).
3	HIGH VOLTAGE TRIGGER	63 mJ, -520 V trigger pulse at ≈ 20 Hz rate. This signal is present when connected to a trigger transformer referenced to pin 8 (high voltage return) and output voltage is open circuit (reference Figure 4).
4	CURRENT SENSE EMITTER	Emitter output of opto coupler which turns on when lamp current is present (reference Figure 2).
5	CURRENT SENSE COLLECTOR	Collector output of opto coupler which turns on when lamp current is present. Do not exceed 35 V, 10 mA (reference Figure 2).
6	ENABLE INPUT	Opto coupled enable input with 470Ω of input impedance. 5 – 20 mA of current to enable module referenced to enable return (reference Figure 3).
7	CASE GROUND	Chassis ground which is not common to either high voltage return or input return.
8	HIGH VOLTAGE OUTPUT & TRIGGER RETURN	High voltage output, and trigger return. This return is fully isolated from input return, and chassis ground (reference Figure 4).
9	ENABLE RETURN	Return for opto-coupled enable input (reference Figure 3).
10	HIGH VOLTAE OUTPUT	High voltage output of simmer (reference Figure 4).

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MODEL 864
INTERFACE CIRCUITS

FIG.1 POWER INPUT

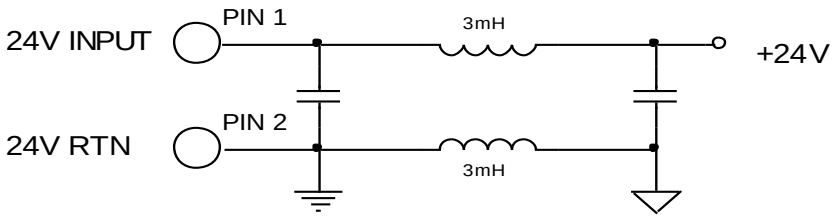


FIG.2 CURRENT SENSE

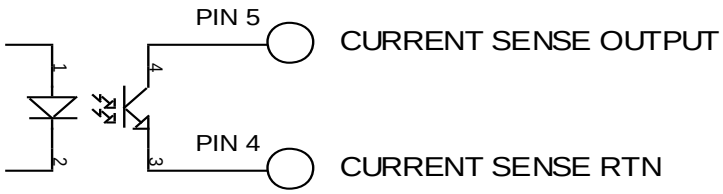


FIG.3 ENABLE

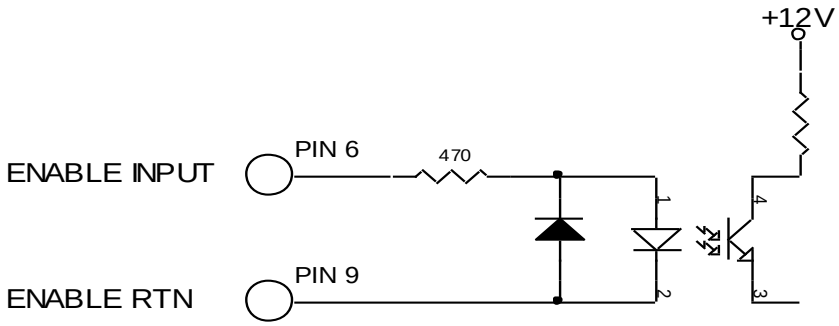
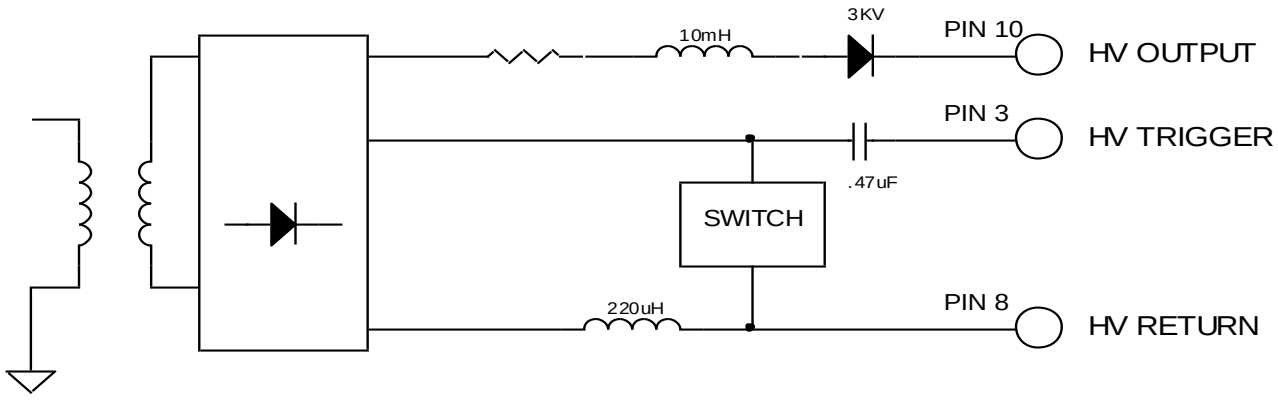


FIG.4 HV/TRIGGER OUTPUT



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Underside Trim-pot Locations